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Electronic packaging - Wikipedia

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23 September 2019. Link added to guidance on how these regulations will change after Brexit. 30 August 2017. First published.

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(engineering) The technology of packaging electronic equipment; in current usage it refers to inserting discrete components, integrated circuits, and MSI and LSI chips (usually attached to a lead frame by beam leads) into plates through holes on multilayer circuit boards (also called cards), where they are soldered in place.

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