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Answer. 4. Q12. In a CB amplifier the maximum efficiency could be. 99%; 85%;

50%; 25%; Answer. 4. Q13. In a pn junction diode under reverse bias, the

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magnitude of electric field is maximum at the edge of the depletion region on the p side; the edge of the depletion region on the n side; the pn junction; the center of the depletion region on the ...

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ANSWERS

ANALOG ELECTRONICS Questions :-1.

The early effect in a bipolar junction transistor is caused by

- A. fast turn-on
- B. fast turn-off
- C. large collector-base reverse bias
- D. large emitter-base forward bias.

2. MOSFET can be used as a

- A. current controlled capacitor
- B. voltage controlled capacitor
- C. current controlled

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Answers D. voltage controlled inductors.

3.

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Answer: Constant current bias circuit
supplies constant emitter current. It also
provides very high source resistance since

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the AC equivalent of the DC current source is ideally an open circuit. Q.15.

What is the use of level translators?

Answer: Because of direct coupling, dc level at the emitters rises from stage to stage. This increase dc level tends to shift the operating point of the succeeding stage and therefore limits the output voltage

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Answers may even distort the output signal.

Analog Circuits Interview Questions and Answers

Answer: A linear integrated circuit is a solid state analog device characterized by a theoretically infinite number of possible operating states. It operates over a

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continuous range of input levels. Within a certain input range, the amplification curve of a linear IC is a straight line i.e. the input and output voltages are directly proportional.

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Gates and Boolean Algebra (10) Long
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(57) Number System (1) Operational
Amplifier (3 ...

Analog Communication Systems

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Amplifiers. Transistor Audio Power
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Let us move to the next Digital Electronics Interview Questions. Q4. Why most interrupts active Low? Answer: The reason for most signals to be active low is as below: If you consider transistor as an

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example, active low means the capacitor in transistor output terminal will get charged or discharged based on signals from low to high or from high to low transitions respectively.

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